

HESC-SERD Manual

***High Efficiency & Smart Charging* Vehicle Power Supply DC to DC Converter**

Manufactured by
TRI-M ENGINEERING

Engineered Solutions for Embedded Applications

Technical Manual

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PREFACE

This manual is for integrators of applications of embedded systems. It contains information on hardware requirements and interconnection to other embedded electronics.

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CHAPTER 1 - INTRODUCTION

1.1 GENERAL DESCRIPTION

The HESC-SERD is a high efficiency, high performance DC to DC 60 watt converter that supplies +5V, -5V, +12V & -12V outputs. The HESC-SERD also includes a flash based microcontroller that supplies advanced power management, smart battery charger and an RS232 serial port. The HESC-SERD is designed for low noise embedded computer systems, has a wide input range of 6-50V(>8:1) and is ideal for battery or unregulated input applications. The HESC-SERD is specifically designed for vehicular applications and has heavy-duty transient suppressors (9000W) that clamp the main input voltage to safe levels, while maintaining normal power supply operation.

The HESC-SERD is a state-of-the-art Mosfet based design that provides outstanding line and load regulation with efficiencies up to 95 percent. Organic Semiconductor Capacitors provide filtering that reduces ripple noises below 20mV. The low noise design makes the HESC-SERD ideal for use aboard aircraft or military applications or wherever EMI or RFI must be minimized. The +5VDC and +12VDC outputs are controlled by a constant off-time current-mode architecture regulator that provides excellent line and load transient response.

The HESC-SERD provides up to four stages of battery charging and can charge Lead-Acid, NiCd, and NiMh batteries and is also SMBus level 3 compatible. Charge currents are up to 4A, and battery charging voltages from 9.5 to 35V.

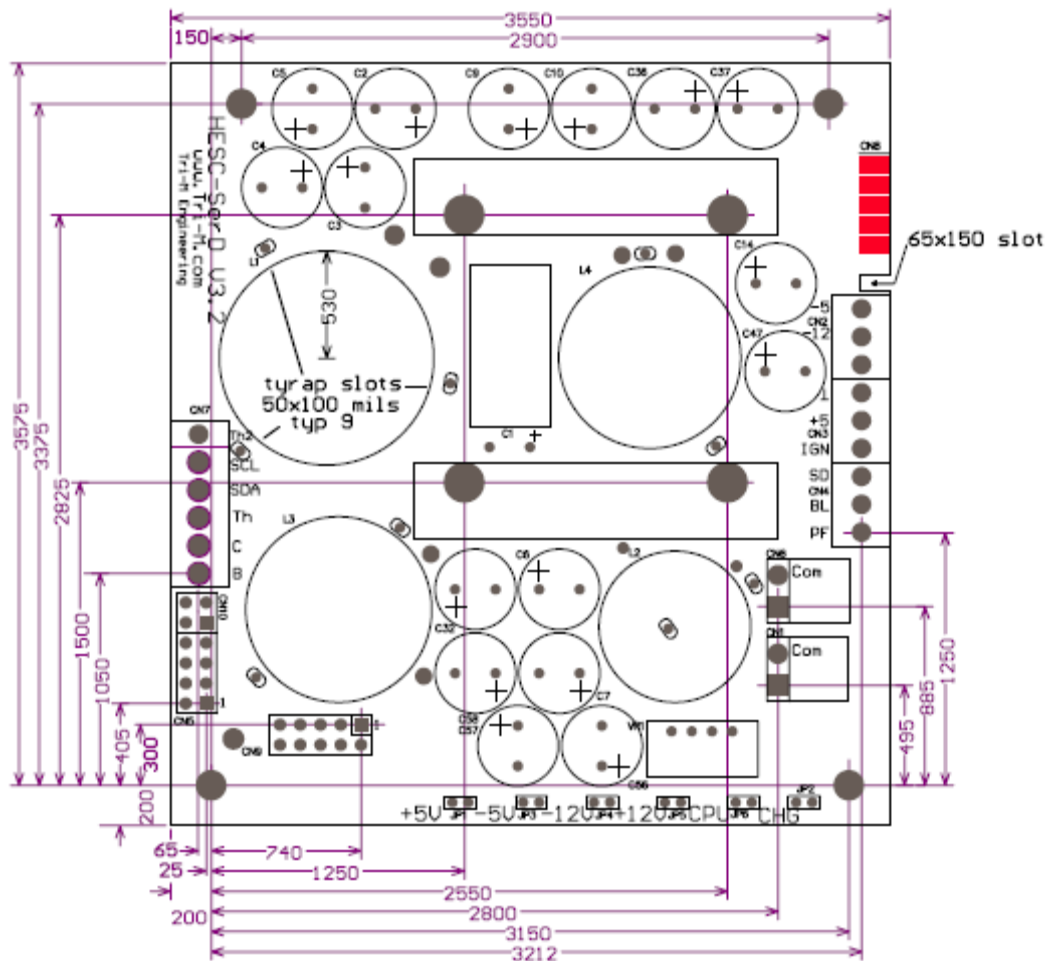
The HESC-SERD has advanced power management functions that allows timed on/off control of the HESC-SERD, notification of changes to main power and changes in the battery status. For example, the HESC-SERD can be programmed to power off the outputs in 60 seconds, and then turn on again 12 hours later.

The HESC-SERD size is 3.55 x 3.775 inches, which is the same size as the PC/104 standard, and has the same mounting holes pattern as the PC/104 standard. However, the HESC-SERD does not supply the PC/104 bus connectors. (For a PC/104 fully compliant power supply, please see the Tri-M Engineering HESC104.) All generated voltages are provided to a removable pluggable header. The removable plug allows the HESC-SERD to be easily installed. The RS232 serial port is provided on a 2x5 row pin header.

The HESC-SERD can be configured to meet almost any power supply and battery charging need for embedded applications, whether that be a simple +5V application, or providing power for back lighted LCD panels, or a full UPS (un-interruptible power supply configuration).

1.2 FEATURES

- DC to DC converter for embedded applications.
- “Load Dump” transient suppression on input power supply.
- Operates from 6VDC to 50VDC input.
- PC/104 size and mounting holes.
- 60 watt power supply outputs.
- 5V, 12V, -12V, -5V, and battery charger outputs.
- Temperature range -40 to 85C.
- Monitors up to 8 external temperatures using I2C digital temperature sensors.
- RS232 serial port for setup, monitoring and control.
- Optocoupled inputs for ignition, and system "shut-down" pushbutton.



1.3 SPECIFICATIONS

Power Supply Specifications	
Model	HESC-SERD
5V output*	12A
12V output	2.5A
-5V output	400mA
-12V output	500mA
Input Voltage Range	6-50V
Load Regulation **	<60mV
Line Regulation **	+40mV
Switching Frequency	75kHz
Max. Input Transient	125V for 100msec
Output Ripple **	<20mV
Conducted Susceptibility **	>57db
Efficiency **	Up to 95%
Temp Range	-40 to 85C
Quiescent current ***	2mA
Size, PC/104 form factor compliant ****	3.55"W x 3.75L x 0.6"H

*Current rating includes current supplied to 12V, -12V & -5V regulators.

** Measured on the 5V output.

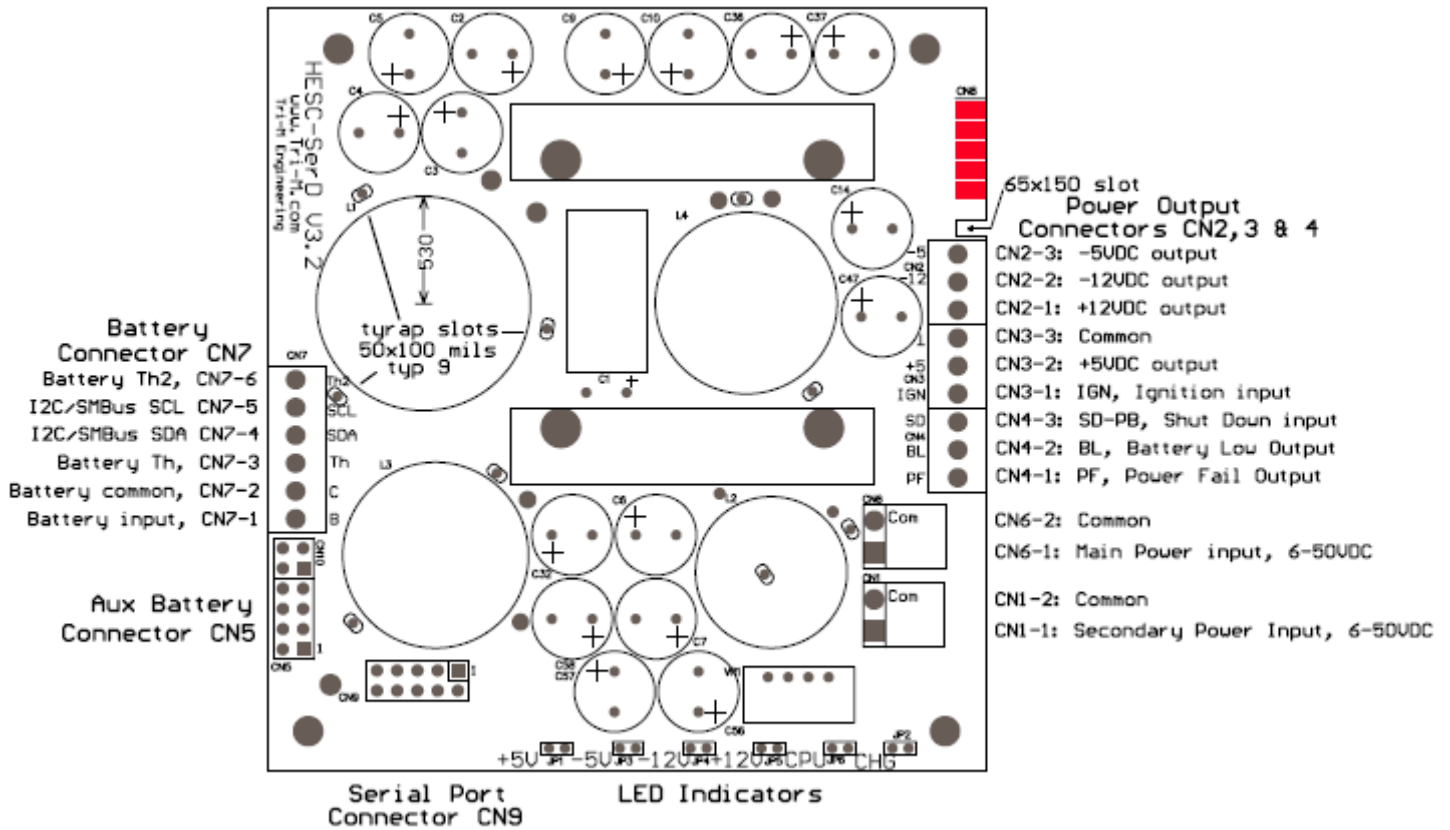
*** LEDS disabled.

**** Height not including stackthrough PC/104 pins.

CHAPTER 2 - CONFIGURATION AND INSTALLATION

2.4 Introduction

This chapter describes the configuration and installation of the HESC-SERD power supply. In addition, section 2.2 provides a formula to calculate the available +5VDC. Figure 2-1 shows the HESC-SERD connectors, jumpers and other options.



Power Considerations.

The +5V switching regulator is rated at 12A maximum output, however the +5V output supplies power to the +12, -5, and -12VDC regulators. To obtain the usable range of +5V output, “derate” according to the use of +12, -5, and -12VDC. Use the following formulae to calculate the maximum usable output.

$$Usable + 5Voutput = 12A - \frac{(I[-5] + I[-12] * 2.4 + I[12] * 2.4)}{0.9}$$

Where: I[-5] = -5VDC current load
 I[-12] = -12VDC current load
 I[12] = 12VDC current load

Assuming 90 percent converter efficiency (actual efficiency may vary).

2.4.1 Main Input Power Connector

Input power is connected to the HESC-SERD by a “Molex” plug and pin connector CN6. The power supply accepts DC input voltages in the range of 6VDC to 50VDC.

Unregulated vehicle power is connected as follows:

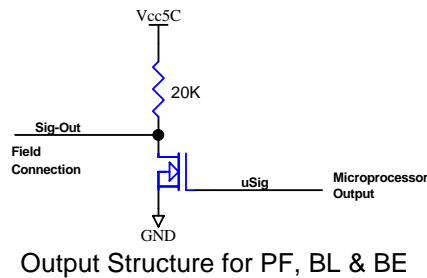
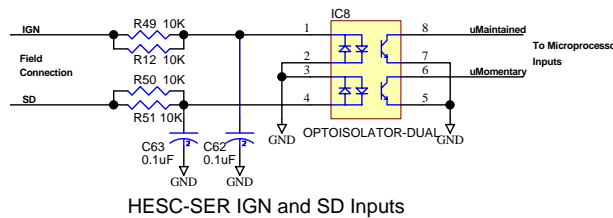
- Terminal 1: “hot” polarity
- Terminal 2: Common (0VDC)

2.4.2 Output Power Connector

Output power is available for use via removable pluggable connector. Connectors CN2, CN3 & CN4 are immediately side by side, with a nine position mating plug-in mating header supplied

- CN4-1: Position 1, PF (Power Fail signal output) TTL logic level
- CN4-2: Position 2, BL (Battery Low signal output) TTL logic level
- CN4-3: Position 3, SD-PB (Shut Down input, ie push button input) 6-50V DC
- CN3-1: Position 4, IGN (Ignition input, ie maintained contact closure) 6-50V DC
- CN3-2: Position 5, +5VDC output
- CN3-3: Position 6, Common
- CN2-1: Position 7, +12VDC output
- CN2-2: Position 8, -12VDC output
- CN2-3: Position 9, -5VDC output

Note: All outputs are active low. The active state of the IGN signal is programmable by using SCU utility.



2.4.3 Battery Connector

Batteries are connected via the Weiland connector, CN7. The HESC-SERD accepts DC battery voltages in the range 6.5V to 35VDC through the Battery Power Connector.

- CN7-1: Battery Positive
- CN7-2: Common
- CN7-3: TH, thermistor/safety input for primary battery.
- CN7-4: SDA, I2C/SMBus data input/output signal
- CN7-5: SCL, I2C/SMBus clock input/output signal
- CN7-6: TH2, thermistor/safety input for secondary battery.

2.4.4 Aux Battery Connector

Tri-M Engineering battery packs such as the BAT104-NiCd, BAT104-NiMh, BAT104-SLA25 and BAT104-SLA45 can be directly plugged into the HESC-SERD through connector CN5. Connector CN5 is a two row by four pin header, with the BAT104 battery packs having a mating female connector.

- CN5-1: Battery Positive
- CN5-2: Common
- CN5-3: Battery Positive
- CN5-4: Common
- CN5-5: SDA, I2C/SMBus data input/output signal
- CN5-6: SCL, I2C/SMBus clock input/output signal
- CN5-7: +5VC, +5V for digital temperature sensor and battery enable
- CN5-8: BE, Battery Enable output

2.4.5 RS232 Serial Port Interface

The HESC-SERD provides an RS232 serial port for remote control, monitoring and datalogging. The serial port connector is a two row by five pin header connector, CN9.

- CN9-1: BL, Battery Low signal output (TTL level)
- CN9-2: Not used
- CN9-3: TX-Out, RS232 output signal
- CN9-4: Not used
- CN9-5: RX-IN, RS232 input signal
- CN9-6: PF-232, Power Fail signal output, RS232 signal levels
- CN9-7: SD-232, Shut Down signal input, RS232 signal levels
- CN9-8: Not used
- CN9-9: Common
- CN9-10: Common

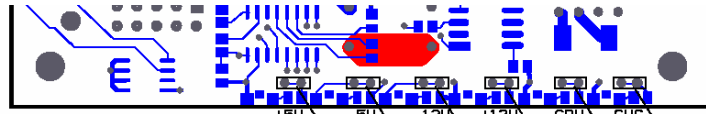
2.5 Jumper Selection

This section describes the function of each jumper, the location of it, the default setting, and how to change it.

2.5.1 LED Jumper Enable/Disable

These jumpers allow the LEDs to be disabled. This is most likely to be used when absolute minimum power consumption must be maintained, such as when operating off a limited battery source.

The location of each LED jumper shown is in the diagram below.



**LED cut trace jumpers
on SMD side of HESC-SER**

Each LED is enabled by factory default. To disable any LED, remove the LED jumper (or cut the small PCB trace if no jumper is installed) associated with the LED. To re-enable any LED, re-install the associated jumper (or solder a short jumper wire between each of the jumper pads).

CHAPTER 3 - Using HESC Power Management Features.

Note: In order to use the advanced power management features, the HESC-SERD must have the HESC-UPS Firmware loaded. Please refer to the HESC-UPS manual for details.

By monitoring and activating the following inputs and outputs, the HESC-SERD power supply is capable of responding to changes in input supply and battery voltage to alert the host CPU of such conditions. To help accomplishing this task with ease, TRI-M provides a Windows based Smart Charger Utility (SCU). This utility is also required when changing the charge profile for different batteries or future firmware updates.

- CN4-1: Position 1, PF (Power Fail signal output)
- CN4-2: Position 2, BL (Battery Low signal output)
- CN4-3: Position 3, SD (Shut Down input, ie momentary push button input)
- CN3-1: Position 4, IGN (Ignition input, ie maintained contact closure)

Note:

1. IGN and SD response to 6-50V DC input as a logic high.
2. The IGN can be triggered on either a logic low or logic high of input. The input polarity of the IGN is set by changing one of the Charger flags.
3. Figure 3-1 is a snap-shot of the SCU utility page which shows the some of charger flags. When the flag is unchecked, IGN reacts to a logic high and vice-versa with flag checked.

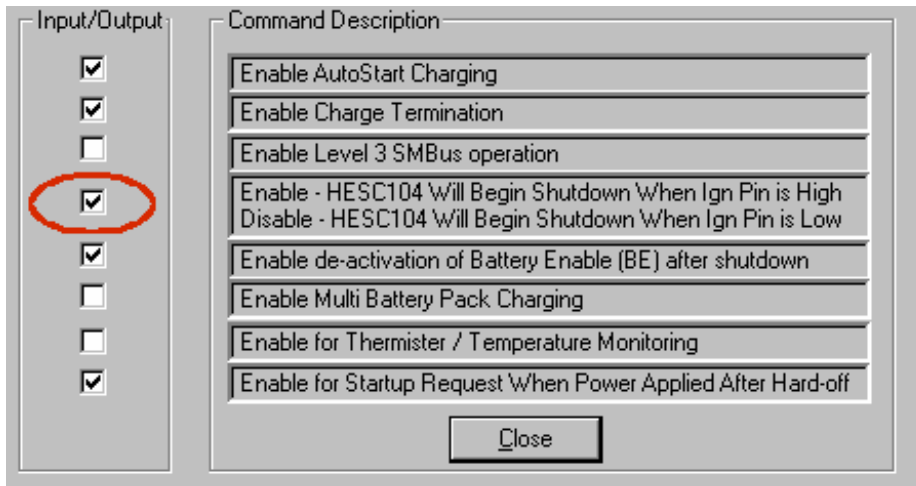


Figure 3-1

BL is driven low when the battery voltage is below the setpoint value of Minimum Battery Operating Voltage EEPROM variable. Figure 3-2 shows this variable under the OTHER EEPROM SETPOINTS page. Please note the unit of 9400 is in millivolt.

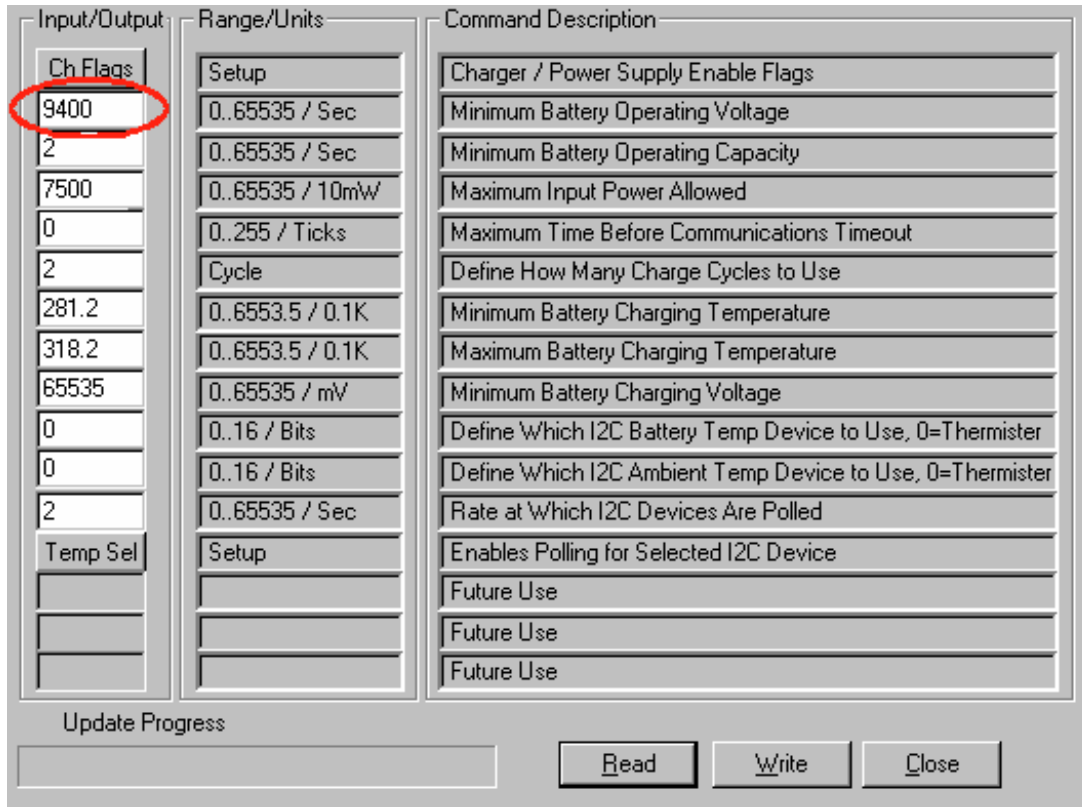


Figure 3-2

PF is driven active low after the main input power is removed and the “debounce” interval is completed or whenever there is a pending shut-down of the main outputs.

IGN, SD, BL and PF can be used to signal the host CPU to prepare for shutdown. It is critical that operating systems such as Linux and Windows are shutdown gracefully otherwise corruption of the OS and the file system may result.

The following diagram summarizes the various input conditions which generate shutdown command to the HESC-SERD. After one of the four signals (BL, PF, SD, IGN) becomes active, the corresponding counter will start counting down to zero. When the counter reaches zero, a shutdown command is issued to switch off the HESC-SERD outputs immediately.

APPENDIX 1 : External Battery Design:

4.1 Battery Isolation

The HESC series products allow for an external backup battery to be connected. For applications where long periods of power interruption may occur, a Mosfet isolation circuit should be used to prevent deep cycling the batteries. Below is a circuit complete with typical component values.

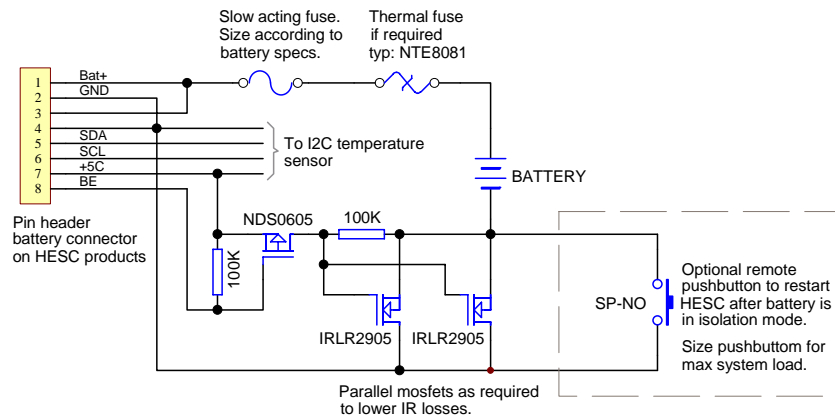


Figure A1: Battery Mosfet Isolation Circuit

4.2 Digital Sensor Interface

The HESC series support I²C digital temperatures of Microchip (TCN75) and National (LM75CIM). These I²C are “two wire” devices and require connection of a bi-directional data line (SDA) and a bidirectional clock line (SCL). In addition, 5V power and Gnd are required. Both SDA and SCL along with 5V and Gnd are available through the eight pin battery header connector (see Figure A1) on HESC products. SDA and SCL are also available on the six position screw terminal block CN7 on the HESC-SERD.

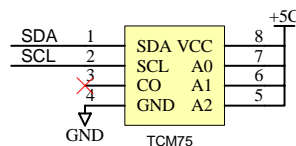


Figure A2: Digital Temperature Sensor