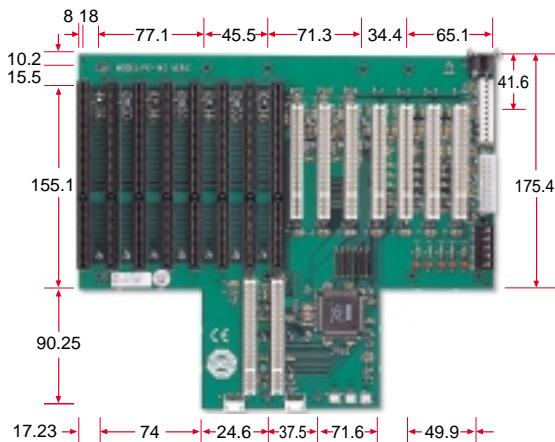
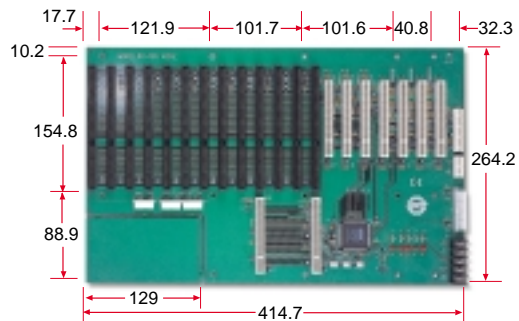


# PX Series PICMG Bus Bridged Backplane

**PX-14S**

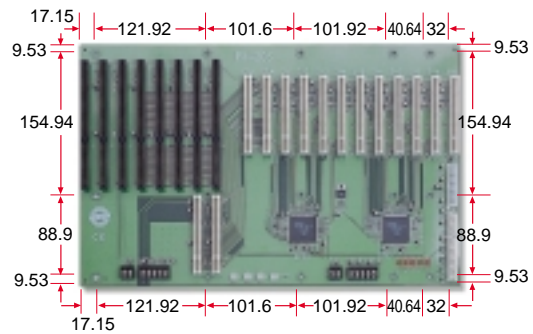


**PX-19S**

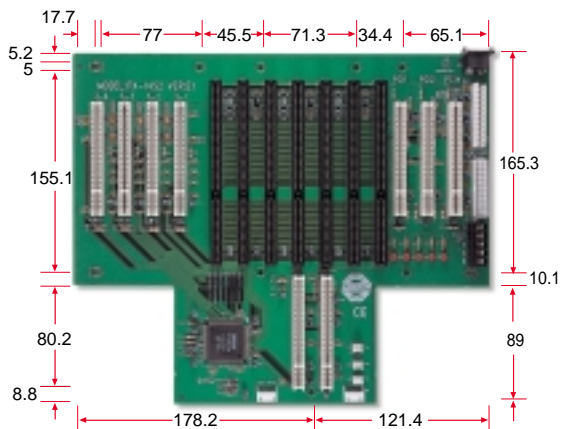


**PX-20S**

*(Optimum PCI bus loading design !)*

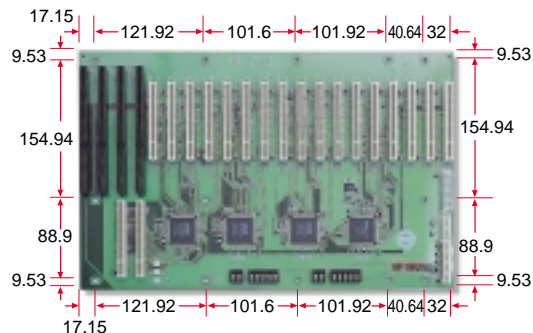


**PX-14S2**

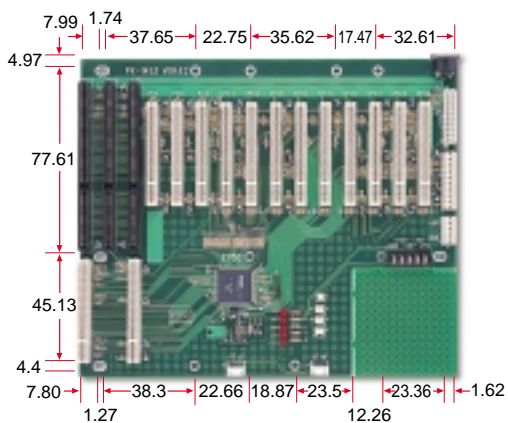


**PX-20S2**

*(Optimum PCI bus loading design !)*



**PX-14S3**



**PX-20S3**

