

Comparison Chart on the line of MachZ based CPU Boards

	ICC-MZ104	MZ104	MZ104+
CPU and System			
Flash BIOS (Phoenix 256Kb, Balance Usable)	2 or 4 MB	2 or 4 MB	2 or 4 MB
144 pin SO-DIMM DRAM	16 / 32 / 64 MB	16 / 32 / 64 MB	16 / 32 / 64 MB
DiskOnChip (32 pin socket)	8 to 288 Mb	8 to 288 Mb	8 to 288 Mb
EIDE Interface	up to 2 devices	up to 2 devices	up to 2 devices
Floppy Disk Interface	1	1	1
Parallel Port (EPP, ECC, IEEE 1284)	1	1	1
Serial Port	COM1: RS232 COM2: RS232, RS485	COM1: RS232 COM2: RS232	COM1: RS232 COM2: RS232
USB	1	1	1
I ² C Bus	1	1	1
Ethernet	100/10BaseT	-	2x 100/10BaseT
PC/104 Interface	16 bit connector	16 bit connector	16 bit connector
PC/104 Plus Interface	-	-	32 bit PCI bus connector
Keyboard/Mouse/Ext Battery Connector	1	1	1
Fail Safe Boot ROM	1	1	1
Dual Watchdog Timer	1	1	1
Data Acquisition			
Analog to Digital Inputs	8 channel, 10 bit resolution	-	-
Digital to Analog Outputs	8 channel, 12 bit resolution	-	-
Optoisolated Input	4 channel, 3 to 28VDC	-	-
Digital Outputs	4 channel, "open drain" FET output, 3 amp @ 24VDC	-	-
Power Supply and Power Management			
DC to DC Power Supply	6 - 40 volt input, 30 watt +5V output	-	-
Smart Battery Charger	9.5 to 19.5V	-	-
Microprocessor Power Management	Flash Based	-	-

Note: Scheduled to release in late January 2001. Specifications may be subject to changes.